

DO NOT ENTER: /JG/ 04/30/2009

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of : **Confirmation No. 3496**
Toshiaki TAKENAKA et al. : Attorney Docket No. 2006_0223A
Serial No. 10/595,157 : Group Art Unit 1791
Filed March 9, 2006 : Examiner John L. Goff, II
METHOD OF MANUFACTURING
MULTI-LAYER CIRCUIT BOARD **Mail Stop: AF**

AMENDMENT AFTER FINAL

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Responsive to the Office Action mailed January 29, 2009, please amend the above-identified application as follows: